| side | | | set |
|------|---|---|------|
| | *DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ* | | |
| L4 | L2 and (predefin$3 or predetermin$3) near4 bus near3 transaction | 24 | L4 |
| L3 | L2 and phase near2 generator and (predefin$3 or predetermin$3) near4 bus near3 transaction | 0 | L3 |
| L2 | L1 and (predetermin$3 or predefin$3) | 1040 | L2 |
| L1 | (instruction or command) near3 (memory or stor$3) and (plurality or multiple or another)near4 (instruction or command) and transaction same bus and (sequence or simultaneous$2) | 1619 | L1 |

END OF SEARCH HISTORY

h　　e　b　　　　b　cg　b　　e　e　ch